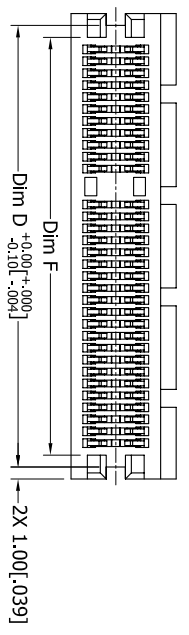
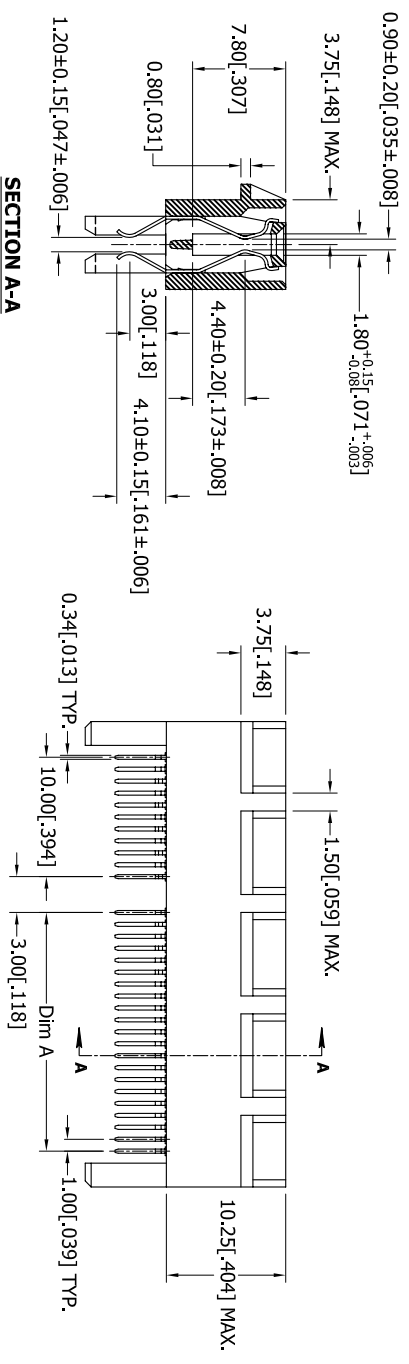
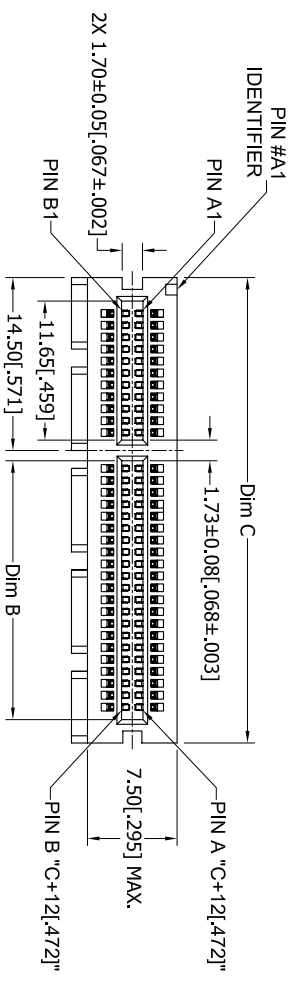
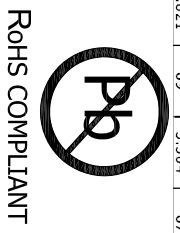


REVISIONS				
REV.	ECO. NO	DESCRIPTION	DATE	BY
A	1595	INITIAL RELEASE	11/31/07	VJ
B	1707	UPDATE P/N CODING CHART, PLATING	05/30/08	ASK



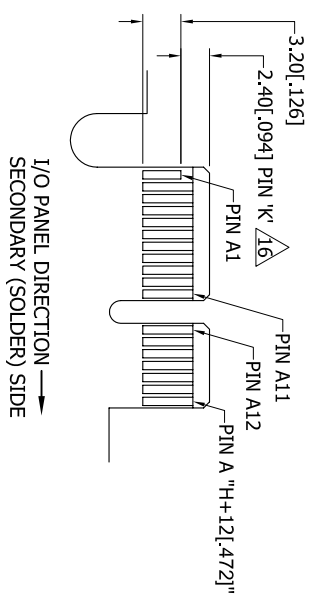
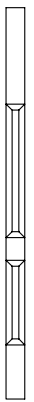
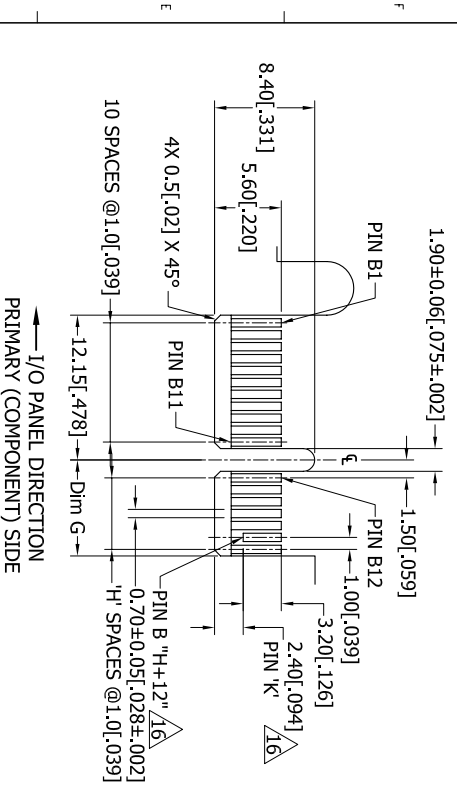
PART NUMBER	POSTIONS/ CONTACTS	A		B		C		D		E		F		G		SPACES 'H'	
		MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH	MM	INCH		
N_EI8DHRN-194-0	18/36	6	0.236	7.65	0.301	25	0.984	23	0.906	9.5	0.374	21.20	0.835	8.15	0.321	6	0.236
N_EI32DHRN-194-0	32/64	20	0.787	21.65	0.852	39	1.535	37	1.457	23.5	0.925	35.20	1.386	22.15	0.872	20	0.787
N_E49DHRN-194-0	49/98	32	1.457	38.65	1.522	56	2.204	54	2.126	40.5	1.594	52.20	2.056	39.15	1.541	37	1.457
N_E82DHRN-194-0	82/164	70	2.756	71.65	2.821	89	3.504	87	3.429	73.5	2.893	85.20	3.356	72.15	2.841	70	2.756

- NOTES:
- INSULATOR MATERIAL: HIGH TEMPERATURE THERMOPLASTIC.
 - CONTACT MATERIAL: SEE PART NUMBER CODING.
 - PLATING: SEE PART NUMBER CODING.
 - OPERATING TEMPERATURE: -10°C ~ 85°C.
 - PROCESSING TEMP: 260°C FOR 10 SECONDS MAX.
 - UL FLAMMABILITY RATING: 94V-0
 - VOLTAGE RATING: 50 VDC MINIMUM AT SEA LEVEL.
 - CURRENT RATING: 1.1 AMP PER CONTACT.
 - VOLTAGE DROP: 44 MILLI VOLT AT RATED CURRENT.
 - INSULATION RESISTANCE: 1000 MEGA OHMS.
 - CONNECTOR IDENTIFICATION: THE PART SHALL BE MARKED WITH A PART NUMBER AND BARCODE.
 - BOARD THICKNESS ACCOMMODATED: 1.57±0.20 [0.62±.008].
 - BOARD INSERTION FORCE: 1.15N (117.3g) MAX PER CONTACT PAIR USING 1.57 [0.62] TEST BLADE.
 - BOARD WITHDRAWAL FORCE: 0.15N (15.3g) MINIMUM PER CONTACT PAIR USING 1.57 [0.62] PCB
 - MODIFICATION: SEE PART NUMBER CODING.
 - NO THE BAR PERMITTED FROM CARD EDGE TO LEADING EDGE OF PAD FOR PINS 'A' AND 'K'.
 - CHAMFER EDGES MUST BE FREE OF CUTTING BURR.



UNLESS OTHERWISE SPECIFIED:
DIMENSIONS ARE IN INCHES(MM)
ANGULAR ±30°
XX±.02 [.508]
XXX±.005 [.127]
SURFACE FINISH: 63 RA
REMOVE ALL BURRS AND SHARP EDGES .010 MAX
INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

SULLINS
 CONNECTOR SOLUTIONS
 DRAWN: 11/31/07 VJ/BAK
 DATE: 11/31/07
 NAME: VJ/BAK
 THE INFORMATION HEREIN CONTAINS SULLINS' TRADE SECRETS AND IS NOT TO BE DISCLOSED TO OTHERS FOR ANY PURPOSES WITHOUT THE WRITTEN PERMISSION OF SULLINS ELECTRONICS.
 REMOVE ALL BURRS AND SHARP EDGES .010 MAX
 INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994
 SCALE: 2:1
 SHEET 1 OF 2



PART NUMBER CODING
 N _ E _ _ DHRN - T94 _ 0

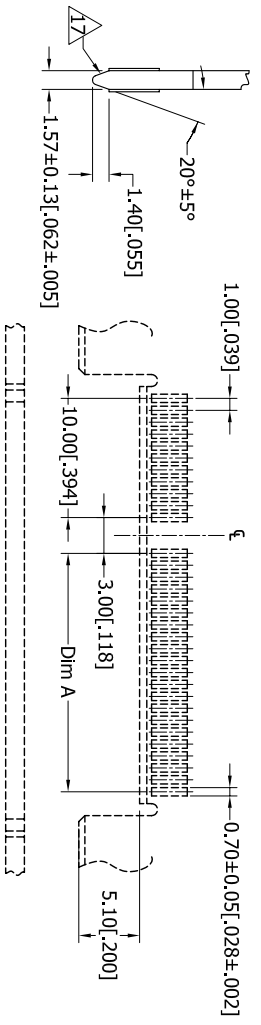
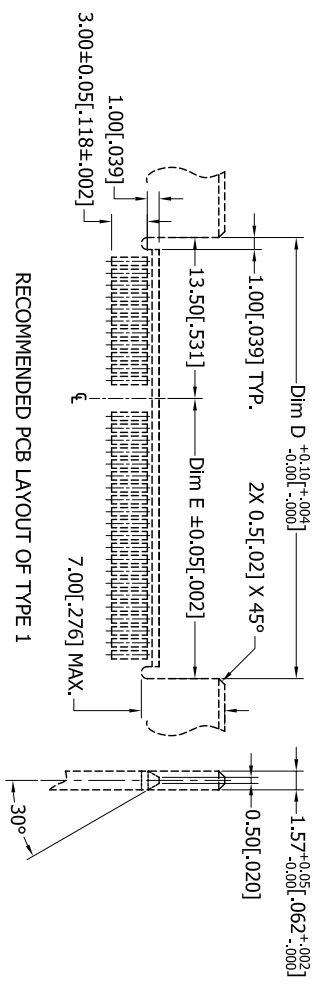
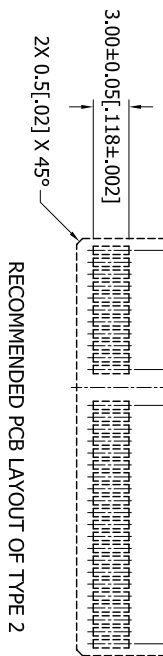
- POSITIONS**
- 18 (X1)
 - 32 (X4)
 - 49 (X8)
 - 82 (X16)

- COLOR**
- 1=BLACK
 - 2=BLUE
 - 3=GREEN
 - 4=PURPLE

PLATING
 ALL PLATINGS ARE LEAD FREE AND HAVE .000050" NICKEL UNDERPLATE ON CONTACT SURFACE AND TERMINATION

CONTACT SURFACE
 B=.000010" GOLD
 C=.000030" GOLD
 W=GOLD FLASH

TERMINATION
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE



RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS ARE IN INCHES(MM)

TOLERANCES:
 ANGULAR: ±.30°
 .XX±.XX (XX=0.0001)
 .XXX±.XXX (XXX=0.0001)
 SURFACE FINISH: 63 Ra
 REMOVE ALL BURRS AND SHARP EDGES: 0.10 MAX

INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

REV	DATE	BY	CHKD	NAME
1	11/21/07	YJAKA		

THE INFORMATION ON THIS SHEET IS UNCLASSIFIED AND IS NOT TO BE RELEASED TO THE PUBLIC WITHOUT THE WRITTEN AUTHORIZATION OF SULLINS CONNECTOR SOLUTIONS.

SIZE: DWG. NO. 11043

SCALE: 2:1



STANDARD MOUNT, W/PEGS, PCIE

PART NUMBER: N_E_DHRN-T94_0

REV B

SHEET 2 OF 2