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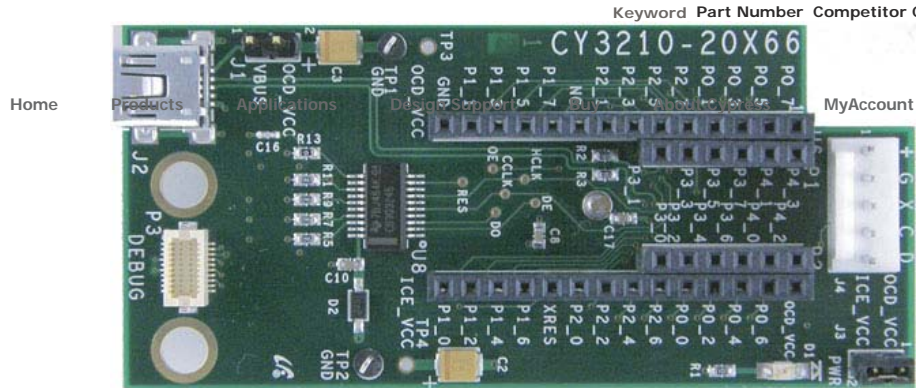
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CY3210 - CY8C20x36/46/66 Evaluation Pod

Last Updated: 06/23/2010

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CY3210-20X66 Kit Contents:

- One (1) CY8C20x36/46/66 Eval Pod

Hardware Description:

PSoC EvalPods are pods that connect to the ICE In-Circuit Emulator (CY3215-DK kit) to allow debugging capability. They can also function as a standalone device without debugging capability. The EvalPod has a 28-pin DIP footprint on the bottom for easy connection to development kits or other hardware. The top of the EvalPod has prototyping headers for easy connection to the devices pins. CY3210-20x66 provides evaluation of the CY8C20x36/46/66 PSoC device family.

翻译文档仅供参考。这些译文可能不是根据最新英文版进行翻译的。我们建议您在做设计开发工作时参阅英文版。

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実際の設計開発には、英語版の文献をご利用いただくことをお勧めいたします。

Related Files

File Title	Language	File Size	Last Updated
📊 CY3210-20X66 Kit BOM.xls	English	64 KB	01/23/09

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