74VCX162244

LOW VOLTAGE CMOS 16-BIT BUS BUFFER (3-STATE) WITH 3.6V TOLERANT INPUTS AND OUTPUTS

- 3.6V TOLERANT INPUTS AND OUTPUTS
- HIGH SPEED :
  t_{PD} = 3.4 ns (MAX.) at V_{CC} = 3.0 to 3.6V
  t_{PD} = 3.8 ns (MAX.) at V_{CC} = 2.3 to 2.7V
  t_{PD} = 5.7 ns (MAX.) at V_{CC} = 1.8V
- POWER DOWN PROTECTION ON INPUTS AND OUTPUTS
- SYMMETRICAL OUTPUT IMPEDANCE:
  $|I_{OH}|$ = I_{OL} = 12mA (MIN) at V_{CC} = 3.0V
  $|I_{OH}|$ = I_{OL} = 8mA (MIN) at V_{CC} = 2.3V
  $|I_{OH}|$ = I_{OL} = 4mA (MIN) at V_{CC} = 1.8V
- 26Ω SERIE RESISTORS IN OUTPUTS
  OPERATING VOLTAGE RANGE:
  V_{CC}(OPR) = 1.8V to 3.6V
- PIN AND FUNCTION COMPATIBLE WITH 74 SERIES 162244
- LATCH-UP PERFORMANCE EXCEEDS 300mA (JESD 17)
- ESD PERFORMANCE:
  HBM > 2000V (MIL STD 883 method 3015);
  MM > 200V

DESCRIPTION

The 74VCX162244 is a low voltage CMOS 16 BIT BUS BUFFER (NON INVERTED) fabricated with sub-micron silicon gate and five-layer metal wiring C²MOS technology. It is ideal for low power and very high speed 1.8 to 3.6V applications; it can be interfaced to 3.6V signal environment for both inputs and outputs.
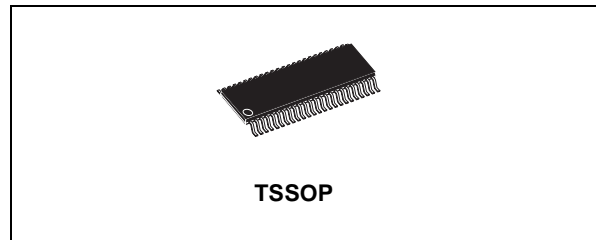
Any nG̅ output control governs four BUS BUFFERS. Output Enable input (nG̅) tied together gives full 16-bit operation.

When nG̅ is LOW, the outputs are on. When nG̅ is HIGH the output are in high impedance state.

This device is designed to be used with 3 state memory address drivers, etc.

The device circuits is including 26Ω series resistance in the outputs. These resistors permits to reduce line noise in high speed applications.
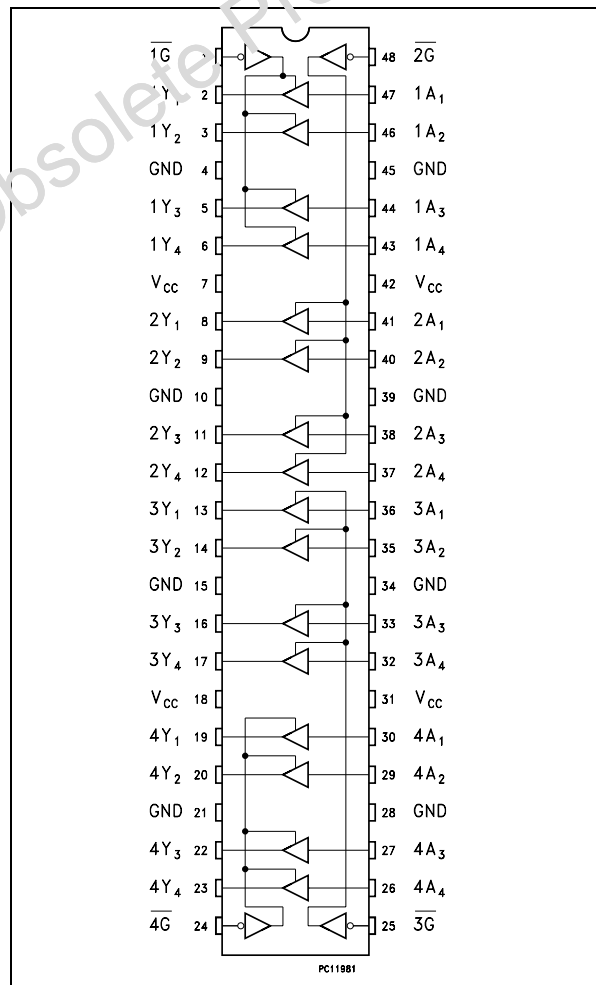
All inputs and outputs are equipped with protection circuits against static discharge, giving them 2KV ESD immunity and transient excess voltage.
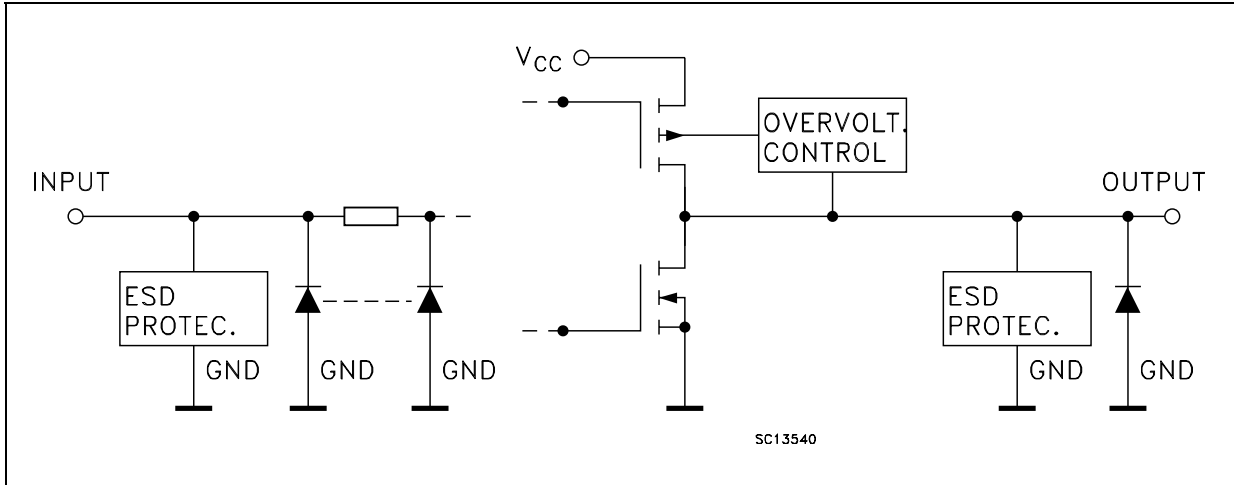

TSSOP

ORDER CODES

PACKAGE	TUBE	T & R
TSSOP		74VCX162244TTR

PIN CONNECTION

1G̅	1	48	2G̅
1Y₁	2	47	1A₁
1Y₂	3	46	1A₂
GND	4	45	GND
1Y₃	5	44	1A₃
1Y₄	6	43	1A₄
Vcc	7	42	Vcc
2Y₁	8	41	2A₁
2Y₂	9	40	2A₂
GND	10	39	GND
2Y₃	11	38	2A₃
2Y₄	12	37	2A₄
3Y₁	13	36	3A₁
3Y₂	14	35	3A₂
GND	15	34	GND
3Y₃	16	33	3A₃
3Y₄	17	32	3A₄
Vcc	18	31	Vcc
4Y₁	19	30	4A₁
4Y₂	20	29	4A₂
GND	21	28	GND
4Y₃	22	27	4A₃
4Y₄	23	26	4A₄
4G̅	24	25	3G̅

PC11981

INPUT AND OUTPUT EQUIVALENT CIRCUIT

SC13540

PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION
1	$1\overline{G}$	Output Enable Input
2, 3, 5, 6	1Y1 to 1Y4	Data Outputs
8, 9, 11, 12	2Y1 to 2Y4	Data Outputs
13, 14, 16, 17	3Y1 to 3Y4	Data Outputs
19, 20, 22, 23	4Y1 to 4Y4	Data Outputs
24	$4\overline{G}$	Output Enable Input
25	$3\overline{G}$	Output Enable Input
30, 29, 27, 26	4A1 to 4A4	Data Outputs
36, 35, 33, 32	3A1 to 3A4	Data Outputs
41, 40, 38, 37	2A1 to 2A4	Data Outputs
47, 46, 44, 43	1A1 to 1A4	Data Outputs
48	$2\overline{G}$	Output Enable Input
4, 10, 15, 21, 28, 34, 39, 45	GND	Ground (0V)
7, 18, 31, 42	V_{CC}	Positive Supply Voltage

TRUTH TABLE

INPUTS		OUTPUT
$\overline{G}$	An	Yn
L	L	L
L	H	H
H	X	Z

X : Don't Care
Z : High Impedance

IEC LOGIC SYMBOLS

LC13550
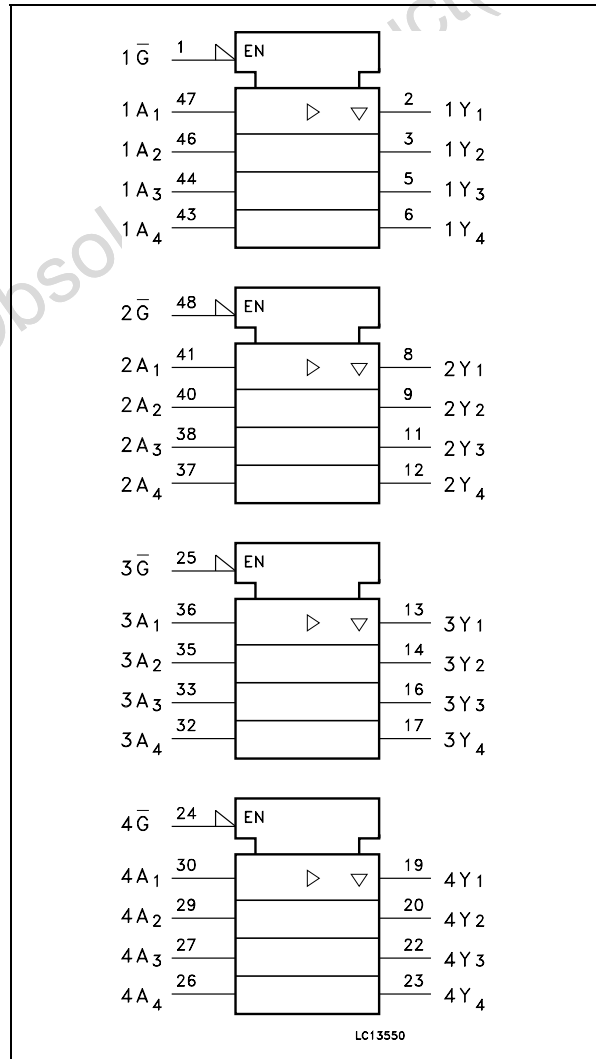
ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	-0.5 to +4.6	V
V_I	DC Input Voltage	-0.5 to +4.6	V
V_O	DC Output Voltage (OFF State)	-0.5 to +4.6	V
V_O	DC Output Voltage (High or Low State) (note 1)	-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current	- 50	mA
I_{OK}	DC Output Diode Current (note 2)	- 50	mA
I_O	DC Output Current	$\pm$ 50	mA
I_{CC} or I_{GND}	DC V_{CC} or Ground Current per Supply Pin	$\pm$ 100	mA
P_D	Power Dissipation	400	mW
T_{stg}	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied
1) I_O absolute maximum rating must be observed
2) V_O < GND, V_O > V_{CC}

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V_{CC}	Supply Voltage	1.8 to 3.6	V
V_I	Input Voltage	-0.3 to 3.6	V
V_O	Output Voltage (OFF State)	0 to 3.6	V
V_O	Output Voltage (High or Low State)	0 to V_{CC}	V
I_{OH}, I_{OL}	High or Low Level Output Current (V_{CC} = 3.0 to 3.6V)	$\pm$ 12	mA
I_{OH}, I_{OL}	High or Low Level Output Current (V_{CC} = 2.3 to 2.7V)	$\pm$ 8	mA
I_{OH}, I_{OL}	High or Low Level Output Current (V_{CC} = 1.8V)	$\pm$ 4	mA
T_{op}	Operating Temperature	-55 to 125	°C
dt/dv	Input Rise and Fall Time (note 1)	0 to 10	ns/V

1) V_{IN} from 0.8V to 2V at V_{CC} = 3.0V

DC SPECIFICATIONS (2.7V < V_{CC} ≤ 3.6V unless otherwise specified)

Symbol	Parameter	Test Condition		Value				Unit
		V_{CC} (V)		-40 to 85 °C		-55 to 125 °C		
				Min.	Max.	Min.	Max.	
V_{IH}	High Level Input Voltage	2.7 to 3.6		2.0		2.0		V
V_{IL}	Low Level Input Voltage				0.8		0.8	
V_{OH}	High Level Output Voltage	2.7 to 3.6	I_O=-100 µA	V_{CC}-0.2		V_{CC}-0.2		V
		2.7	I_O=-6 mA	2.2		2.2		
		3.0	I_O=-8 mA	2.4		2.4		
			I_O=-12 mA	2.2		2.2		
V_{OL}	Low Level Output Voltage	2.7 to 3.6	I_O=100 µA		0.2		0.2	V
		2.7	I_O=6 mA		0.4		0.4	
		3.0	I_O=8 mA		0.5		0.5	
			I_O=12 mA		0.8		0.8	
I_I	Input Leakage Current	2.7 to 3.6	V_I = 0 to 3.6V		± 5		± 5	µA
I_{off}	Power Off Leakage Current	0	V_I or V_O = 0 to 3.6V		10		10	µA
I_{OZ}	High Impedance Output Leakage Current	2.7 to 3.6	$V_I = V_{IH}$ or V_{IL} V_O = 0 to 3.6V		± 10		± 10	µA
I_{CC}	Quiescent Supply Current	2.7 to 3.6	$V_I = V_{CC}$ or GND		20		20	µA
			V_I or $V_O = V_{CC}$ to 3.6V		± 20		± 20	
ΔI_{CC}	I_{CC} incr. per Input	2.7 to 3.6	$V_{IH} = V_{CC}$ - 0.6V		750		750	µA

DC SPECIFICATIONS (2.3V < V_{CC} ≤ 2.7V unless otherwise specified)

Symbol	Parameter	Test Condition		Value				Unit
		V_{CC} (V)		-40 to 85 °C		-55 to 125 °C		
				Min.	Max.	Min.	Max.	
V_{IH}	High Level Input Voltage	2.3 to 2.7		1.6		1.6		V
V_{IL}	Low Level Input Voltage				0.7		0.7	
V_{OH}	High Level Output Voltage	2.3 to 2.7	I_O=-100 µA	V_{CC}-0.2		V_{CC}-0.2		V
		2.3	I_O=-4 mA	2.0		2.0		
			I_O=-6 mA	1.8		1.8		
			I_O=-8 mA	1.7		1.7		
V_{OL}	Low Level Output Voltage	2.3 to 2.7	I_O=100 µA		0.2		0.2	V
		2.3	I_O=6 mA		0.4		0.4	
			I_O=8 mA		0.6		0.6	
I_I	Input Leakage Current	2.3 to 2.7	V_I = 0 to 3.6V		± 5		± 5	µA
I_{off}	Power Off Leakage Current	0	V_I or V_O = 0 to 3.6V		10		10	µA
I_{OZ}	High Impedance Output Leakage Current	2.3 to 2.7	V_I = V_{IH} or V_{IL} V_O = 0 to 3.6V		± 10		± 10	µA
I_{CC}	Quiescent Supply Current	2.3 to 2.7	V_I = V_{CC} or GND		20		20	µA
			V_I or V_O = V_{CC} to 3.6V		± 20		± 20	

DC SPECIFICATIONS (1.8V ≤ V_{CC} ≤ 2.3V unless otherwise specified)

Symbol	Parameter	Test Condition		Value				Unit
		V_{CC} (V)		-40 to 85 °C		-55 to 125 °C		
				Min.	Max.	Min.	Max.	
V_{IH}	High Level Input Voltage	1.8 to 2.3		0.7 VCC		0.7 VCC		V
V_{IL}	Low Level Input Voltage				0.2 VCC		0.2 VCC	V
V_{OH}	High Level Output Voltage	1.8	I_O=-100 µA	V_{CC}-0.2		V_{CC}-0.2		V
			I_O=-4 mA	1.4		1.4		
V_{OL}	Low Level Output Voltage	1.8	I_O=100 µA		0.2		0.2	V
			I_O=4 mA		0.3		0.3	
I_I	Input Leakage Current	1.8	V_I = 0 to 3.6V		± 5		± 5	µA
I_{off}	Power Off Leakage Current	0	V_I or V_O = 0 to 3.6V		10		10	µA
I_{OZ}	High Impedance Output Leakage Current	1.8	V_I = V_{IH} or V_{IL} V_O = 0 to 3.6V		± 10		± 10	µA
I_{CC}	Quiescent Supply Current	1.8	V_I = V_{CC} or GND		20		20	µA
			V_I or V_O = V_{CC} to 3.6V		± 20		± 20	

DYNAMIC SWITCHING CHARACTERISTICS (T_a = 25°C, Input t_r = t_f = 2.0ns, C_L = 30pF, R_L = 500Ω)

Symbol	Parameter	Test Condition		Value			Unit
		V_{CC} (V)		T_A = 25 °C			
				Min.	Typ.	Max.	
V_{OLP}	Dynamic Low Voltage Quiet Output (note 1, 3)	1.8	V_{IL} = 0V V_{IH} = V_{CC}		0.15		V
		2.5			0.25		
		3.3			0.35		
V_{OLV}	Dynamic Low Voltage Quiet Output (note 1, 3)	1.8	V_{IL} = 0V V_{IH} = V_{CC}		-0.15		V
		2.5			-0.25		
		3.3			-0.35		
V_{OHV}	Dynamic High Voltage Quiet Output (note 2, 3)	1.8	V_{IL} = 0V V_{IH} = V_{CC}		1.55		V
		2.5			2.05		
		3.3			2.65		

1) Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH to LOW or LOW to HIGH. The remaining output is measured in the LOW state.
2) Number of outputs defined as "n". Measured with "n-1" outputs switching from HIGH to LOW or LOW to HIGH. The remaining output is measured in the HIGH state.
3) Parameters guaranteed by design.

AC ELECTRICAL CHARACTERISTICS (C_L = 30pF, R_L = 500Ω, Input t_r = t_f = 2.0ns)

Symbol	Parameter	Test Condition		Value				Unit
		V_{CC} (V)		-40 to 85 °C		-55 to 125 °C		
				Min.	Max.	Min.	Max.	
t_{PLH} t_{PHL}	Propagation Delay Time	1.8		1.5	5.7	1.5	6.2	ns
		2.3 to 2.7		1.0	3.8	1.0	4.3	
		3.0 to 3.6		0.8	3.4	0.8	4.0	
t_{PZL} t_{PZH}	Output Enable Time	1.8		1.5	6.7	1.5	7.3	ns
		2.3 to 2.7		1.0	5.1	1.0	5.8	
		3.0 to 3.6		0.8	3.8	0.8	4.3	
t_{PLZ} t_{PHZ}	Output Disable Time	1.8		1.5	5.0	1.5	5.9	ns
		2.3 to 2.7		1.0	4.0	1.0	4.7	
		3.0 to 3.6		0.8	3.6	0.8	4.2	
t_{OSLH} t_{OSHL}	Output To Output Skew Time (note1, 2)	1.8			0.5		0.5	ns
		2.3 to 2.7			0.5		0.5	
		3.0 to 3.6			0.5		0.5	

1) Skew is defined as the absolute value of the difference between the actual propagation delay for any two outputs of the same device switching in the same direction, either HIGH or LOW (t_{OSLH} = | t_{PLHm} - t_{PLHn}|, t_{OSHL} = | t_{PHLm} - t_{PHLn}|)
2) Parameter guaranteed by design

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition		Value			Unit
		V_{CC} (V)		$T_A = 25\ °C$			
				Min.	Typ.	Max.	
C_{IN}	Input Capacitance	1.8, 2.5 or 3.3	$V_{IN} = 0$ or V_{CC}		6		pF
C_{OUT}	Output Capacitance	1.8, 2.5 or 3.3	$V_{IN} = 0$ or V_{CC}		7		pF
C_{PD}	Power Dissipation Capacitance (note 1)	1.8, 2.5 or 3.3	$f_{IN} = 10MHz$ $V_{IN} = 0$ or V_{CC}		20		pF

1) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/16$ (per circuit)

TEST CIRCUIT

SC11690
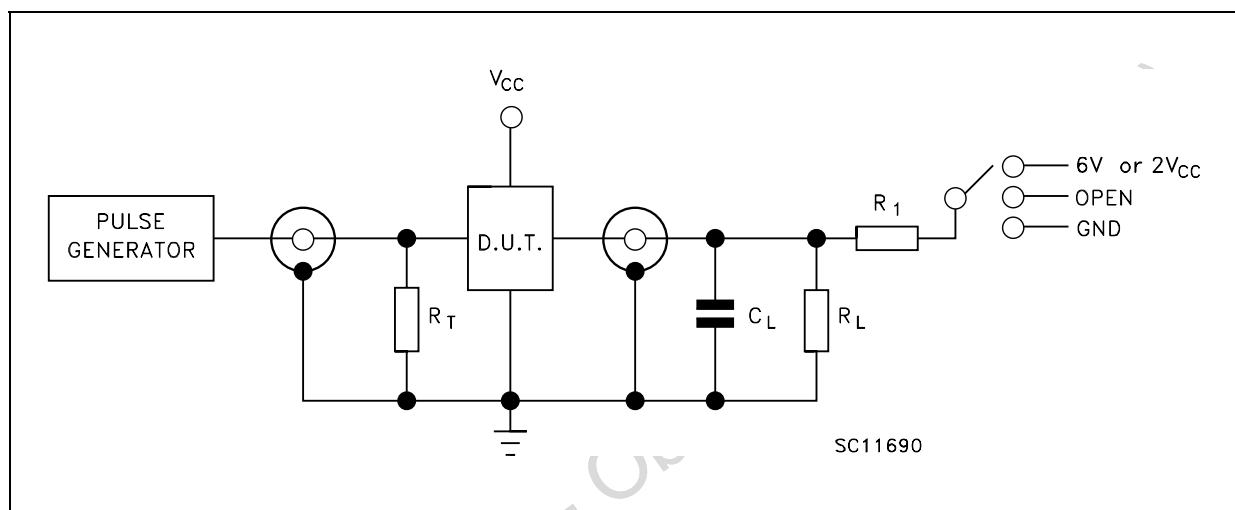
TEST	SWITCH
t_{PLH}, t_{PHL}	Open
t_{PZL}, t_{PLZ} (V_{CC} = 3.0 to 3.6V)	6V
t_{PZL}, t_{PLZ} (V_{CC} = 2.3 to 2.7V or 1.8V)	$2V_{CC}$
t_{PZH}, t_{PHZ}	GND

C_L = 30 pF or equivalent (includes jig and probe capacitance)
R_L = R1 = 500Ω or equivalent
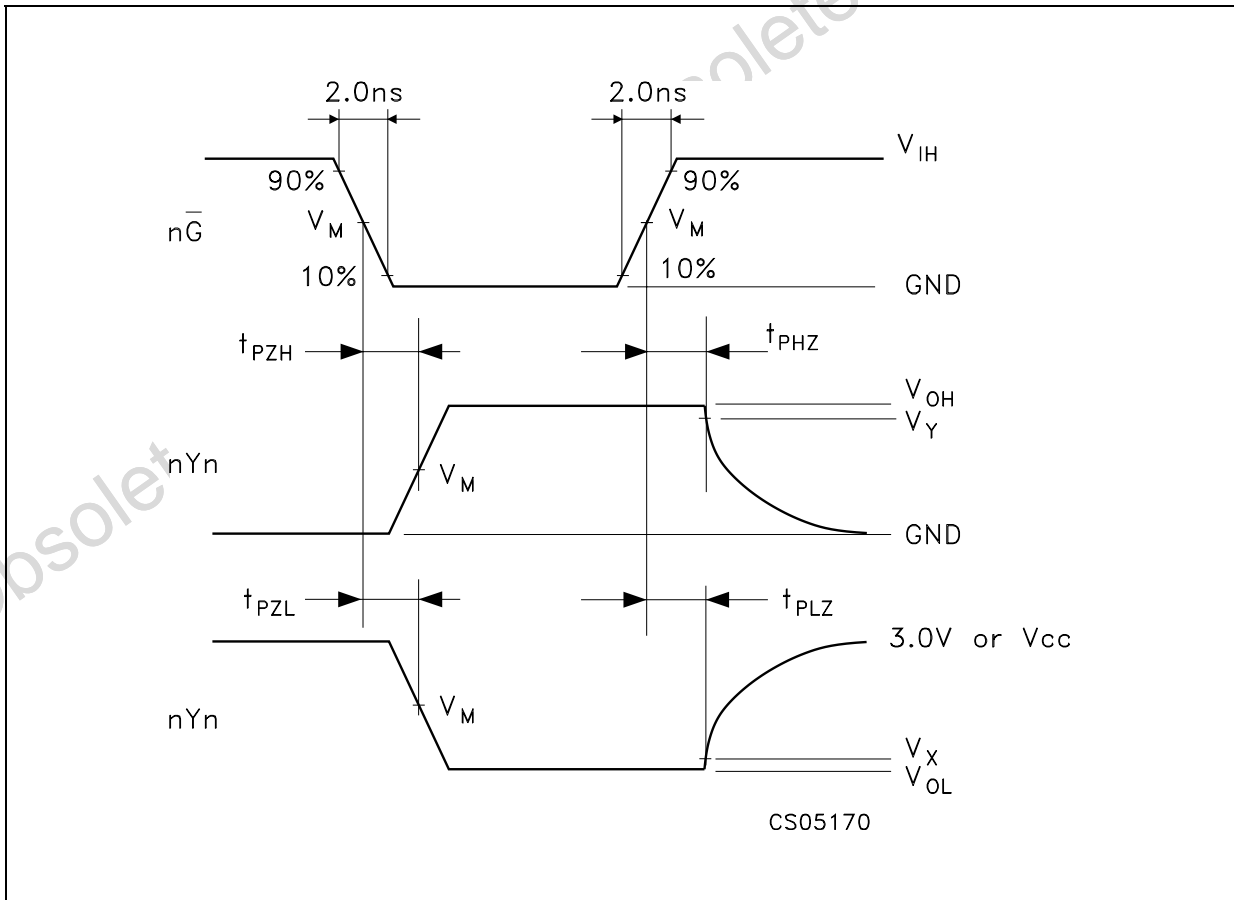R_T = Z_{OUT} of pulse generator (typically 50Ω)

WAVEFORM SYMBOL VALUES

Symbol	V_{CC}		
	3.0 to 3.6V	2.3 to 2.7V	1.8V
V_{IH}	2.7V	V_{CC}	V_{CC}
V_M	1.5V	$V_{CC}/2$	$V_{CC}/2$
V_X	$V_{OL} + 0.3V$	$V_{OL} + 0.15V$	$V_{OL} + 0.15V$
V_Y	$V_{OH} - 0.3V$	$V_{OH} - 0.15V$	$V_{OH} - 0.15V$

WAVEFORM 1: PROPAGATION DELAYS (f=1MHz; 50% duty cycle)

SC11670
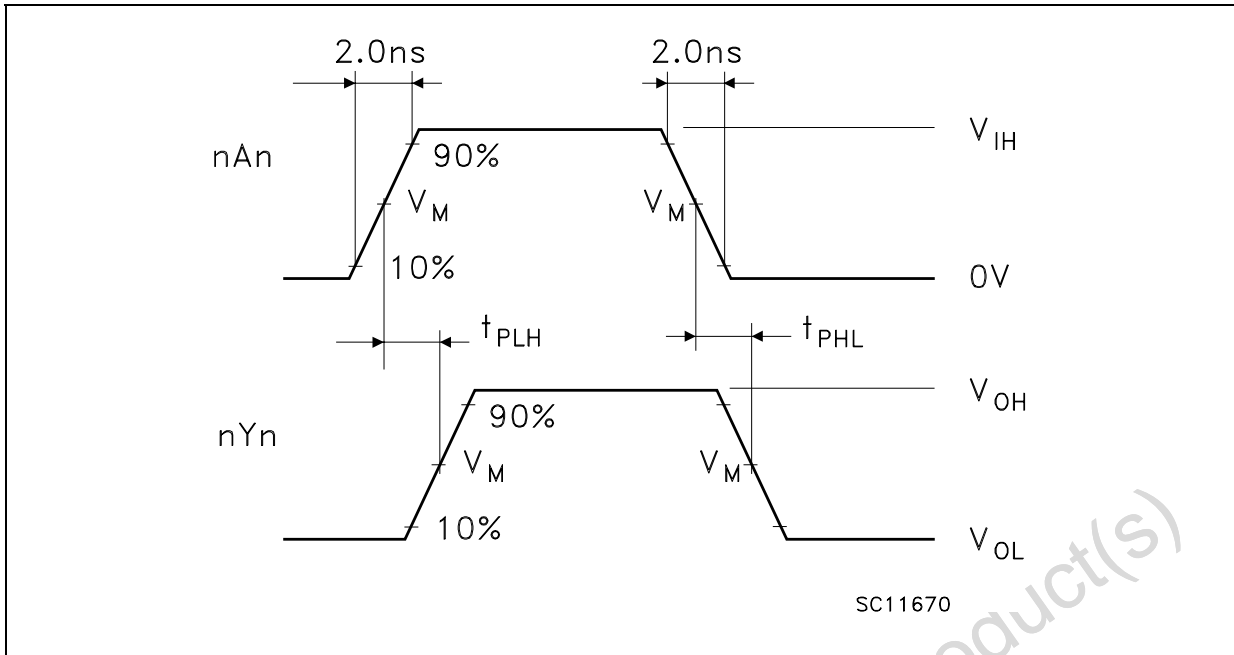
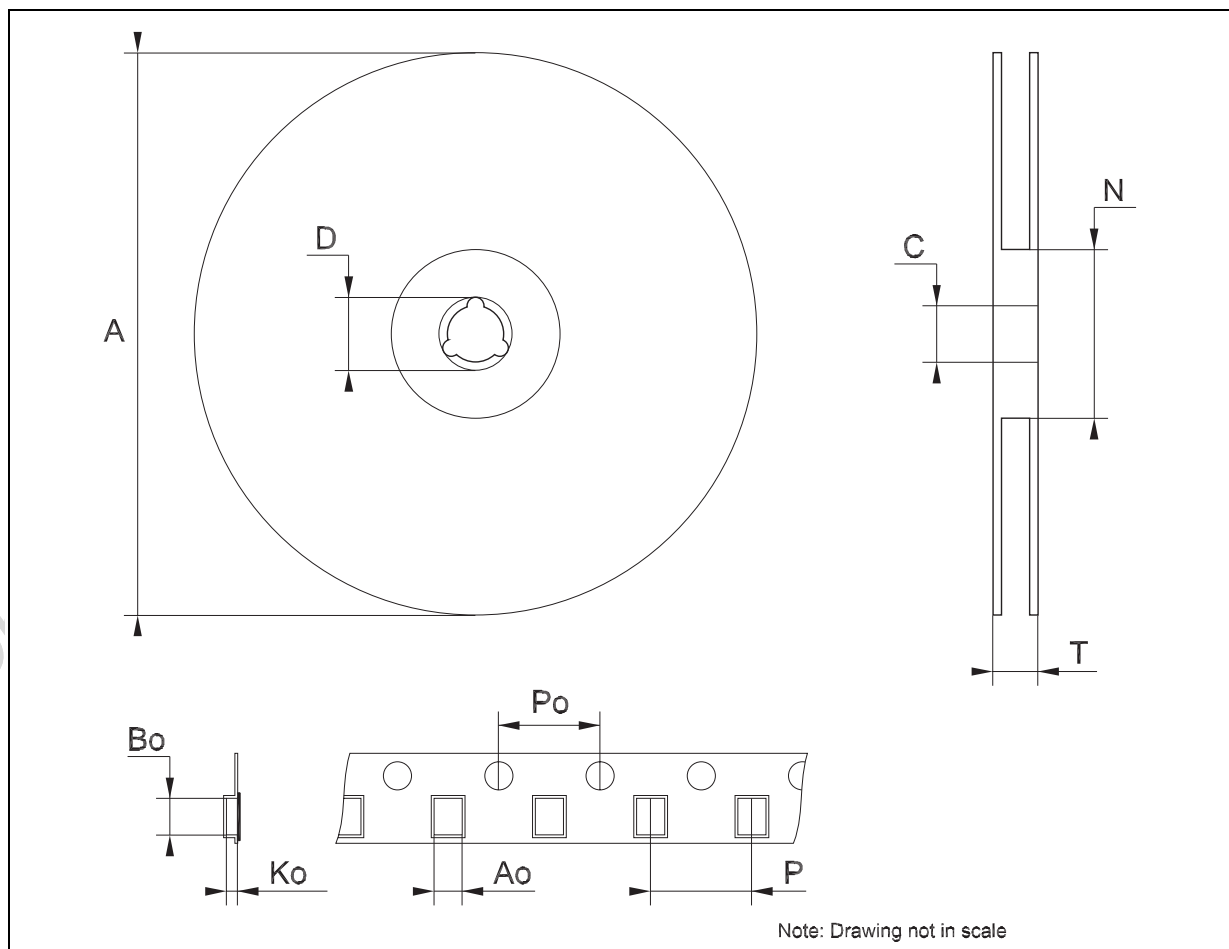**WAVEFORM 2: OUTPUT ENABLE AND DISABLE TIME** (f=1MHz; 50% duty cycle)

CS05170

TSSOP48 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			1.2			0.047
A1	0.05		0.15	0.002		0.006
A2		0.9			0.035	
b	0.17		0.27	0.0067		0.011
c	0.09		0.20	0.0035		0.0079
D	12.4		12.6	0.488		0.496
E		8.1 BSC			0.318 BSC	
E1	6.0		6.2	0.236		0.244
e		0.5 BSC			0.0197 BSC	
K	0°		8°	0°		8°
L	0.50		0.75	0.020		0.030

PIN 1 IDENTIFICATION

7065588C
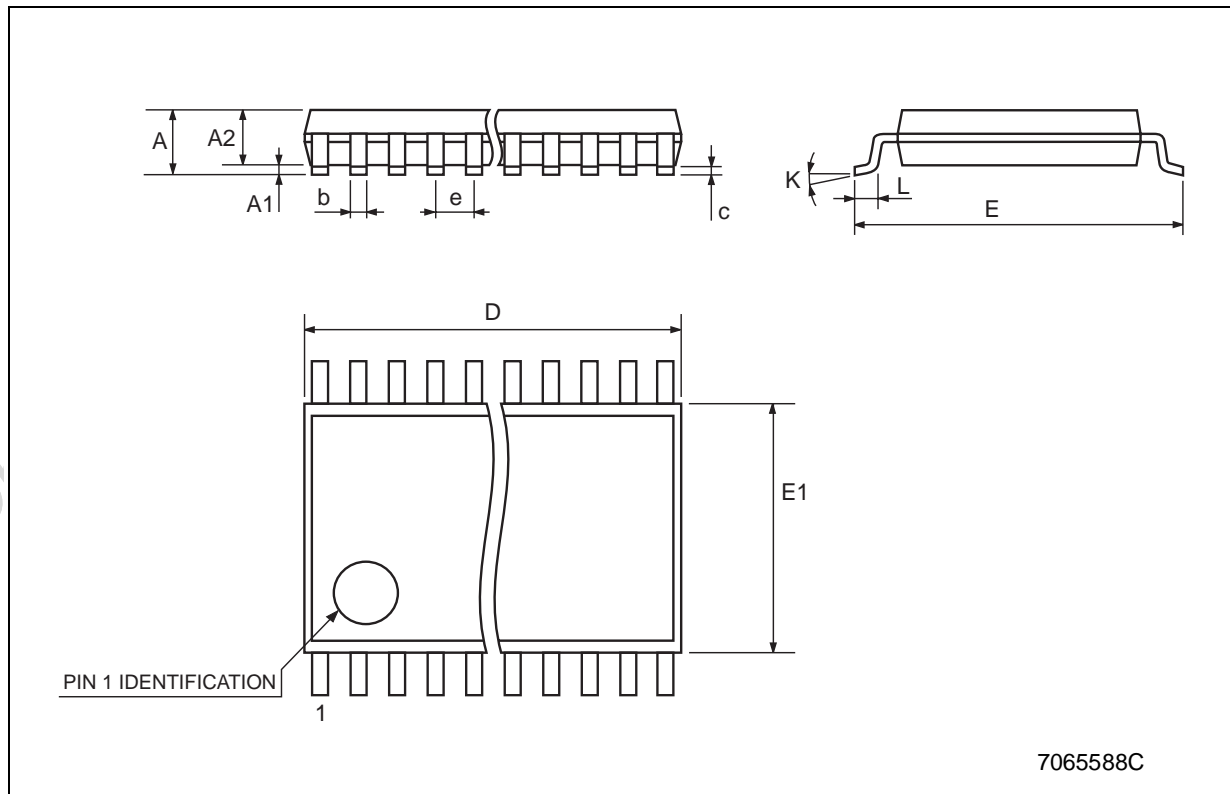
Tape & Reel TSSOP48 MECHANICAL DATA

DIM.	mm.			inch		
	MIN.	TYP	MAX.	MIN.	TYP.	MAX.
A			330			12.992
C	12.8		13.2	0.504		0.519
D	20.2			0.795		
N	60			2.362		
T			30.4			1.197
Ao	8.7		8.9	0.343		0.350
Bo	13.1		13.3	0.516		0.524
Ko	1.5		1.7	0.059		0.067
Po	3.9		4.1	0.153		0.161
P	11.9		12.1	0.468		0.476

Note: Drawing not in scale