

Silicon identification

This errata sheet applies to the STMicroelectronics S2-LP device; see the below information for details.

Table 1: Device identification

Order code	Package	Device info (stored in 0xF0 and 0xF1 registers)	Comment
S2-LPQTR	QFN24 4x4x1	0x0391	Cut 2.1

Contents

1	SDO pin not tri-stated	3
1.1	Part number affected.....	3
1.2	Description	3
1.3	Workaround.....	3
2	Revision history	4

1 SDO pin not tri-stated

1.1 Part number affected

S2-LPQTR cut 2.1.

1.2 Description

The SDO pin is always forced to output with an internal low impedance and the pin is not tri-stated when the CSn pin is high. This prevents the external microcontroller to use the same SPI bus for driving other SPI slaves.

1.3 Workaround

Two actions can be taken:

- do not use multiple slaves on the SPI bus where the S2-LP is attached;
- each time an application needs to access another SPI slave, turn the S2-LP off via the SDN pin.

2 Revision history

Table 2: Document revision history

Date	Revision	Changes
09-Nov-2016	1	Initial release

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